

FIGURE 1

Address[7:0]	Data[63:0]	
00-CF	Packet Data	~12
D0	Port 0 Transmit Descriptor	~14
D1	Port 1 Transmit Descriptor	
D2	Port 2 Transmit Descriptor	
D3	Port 3 Transmit Descriptor	
D4	Port 4 Transmit Descriptor	
D5	Port 5 Transmit Descriptor	
D6	Port 6 Transmit Descriptor	
D7	Port 7 Transmit Descriptor	
D8	Port 8 Transmit Descriptor	~16
D9-DF	Unused	
E0-FF	32 ARL Table Entries	~18

FIGURE 2

Address[16:0]	Data[63:0]	
0000-00FF	Memory Block 0	~20
0100-01FF	Memory Block 1	
0200-02FF	Memory Block 2	
0300-FEFF	Memory Block 3 to 2046	
FF00-FFFF	Memory Block 2047	
10000-1FFFF	Optional Memory Block 2048-4095	

FIGURE 3

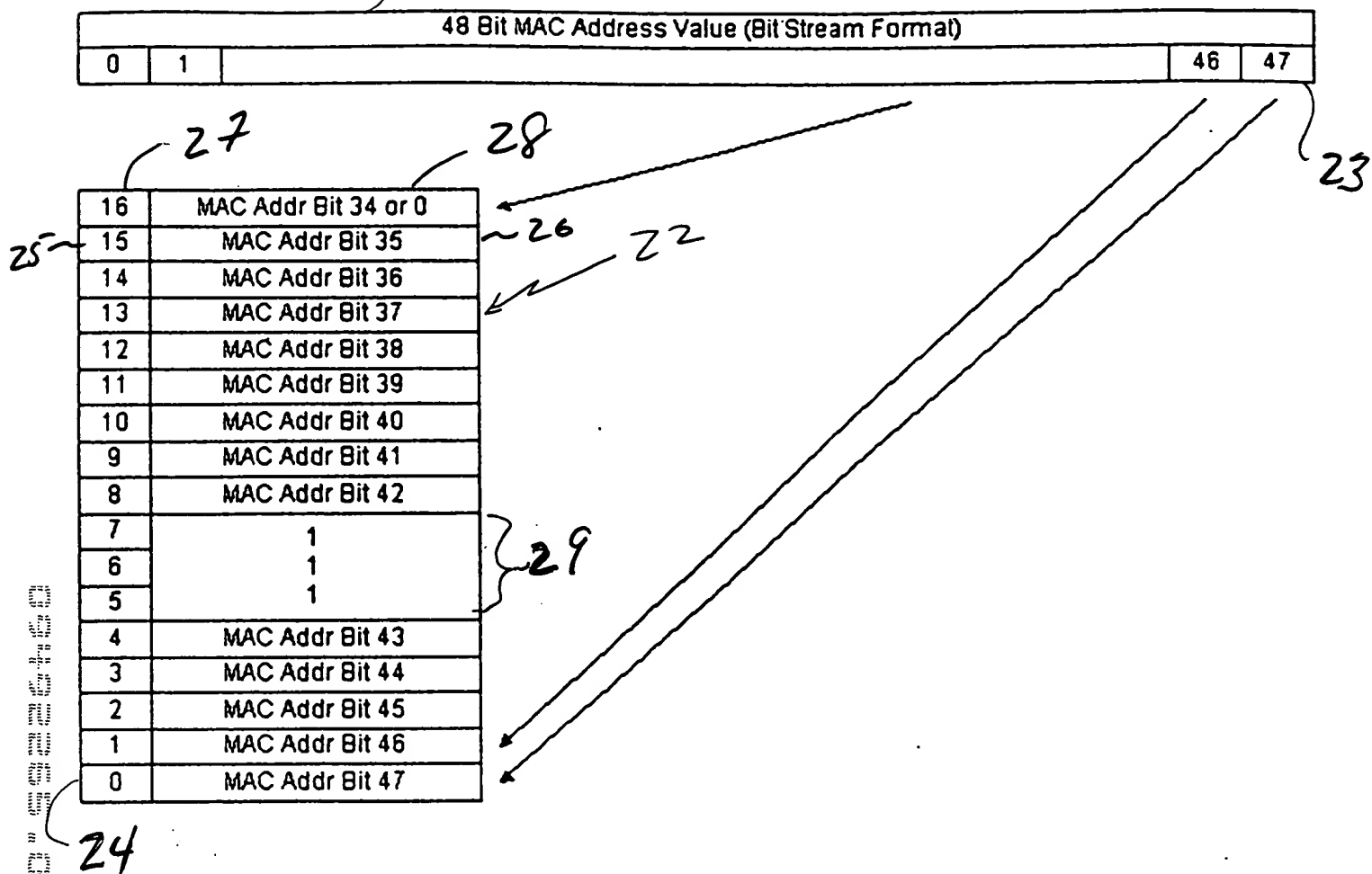


FIGURE 4

Address[7:0]	Data[63:0]							
	[63:56]	[55:48]	[47:40]	[39:32]	[31:24]	[23:16]	[15:8]	[7:0]
00	B0	B1	B2	B3	B4	B5	B6	B7
01	B8	B9	B10	B11	B12	B13	B14	B15
..	-	-	-	-	-	-	-	-
07	B56	B57	B58	B59	B60	B61	B62	B63
08	B64	B65	Unused					
08-CF	Unused							

FIGURE 5

00000000000000000000000000000000

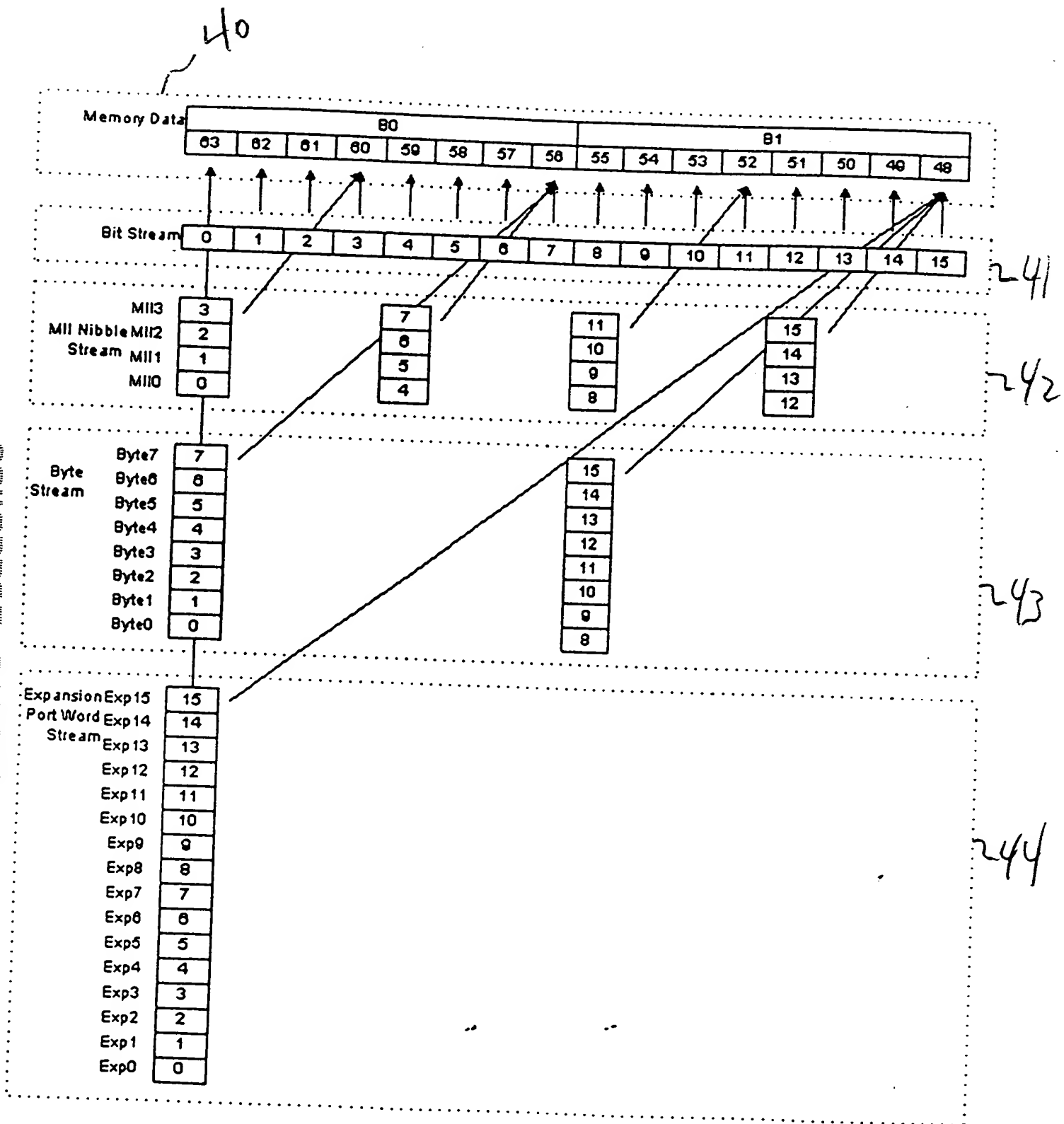
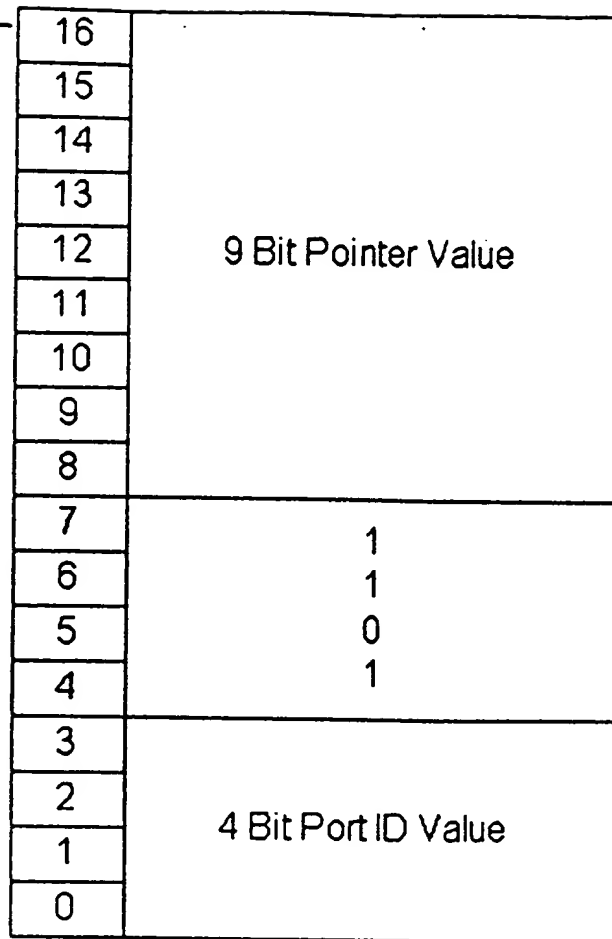


FIGURE 6



Transmit Descriptor Pointer Address

FIGURE 7

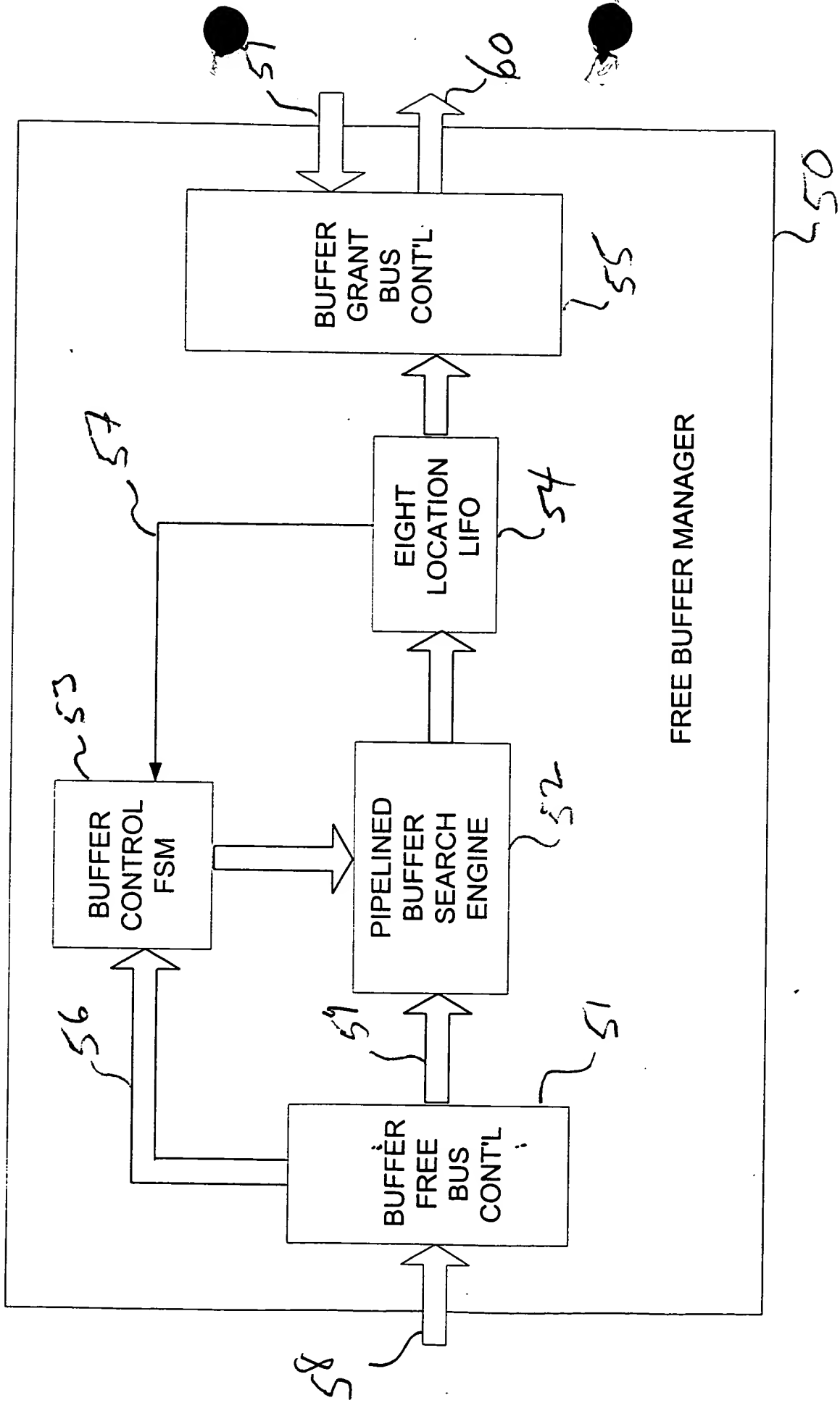


FIGURE 8

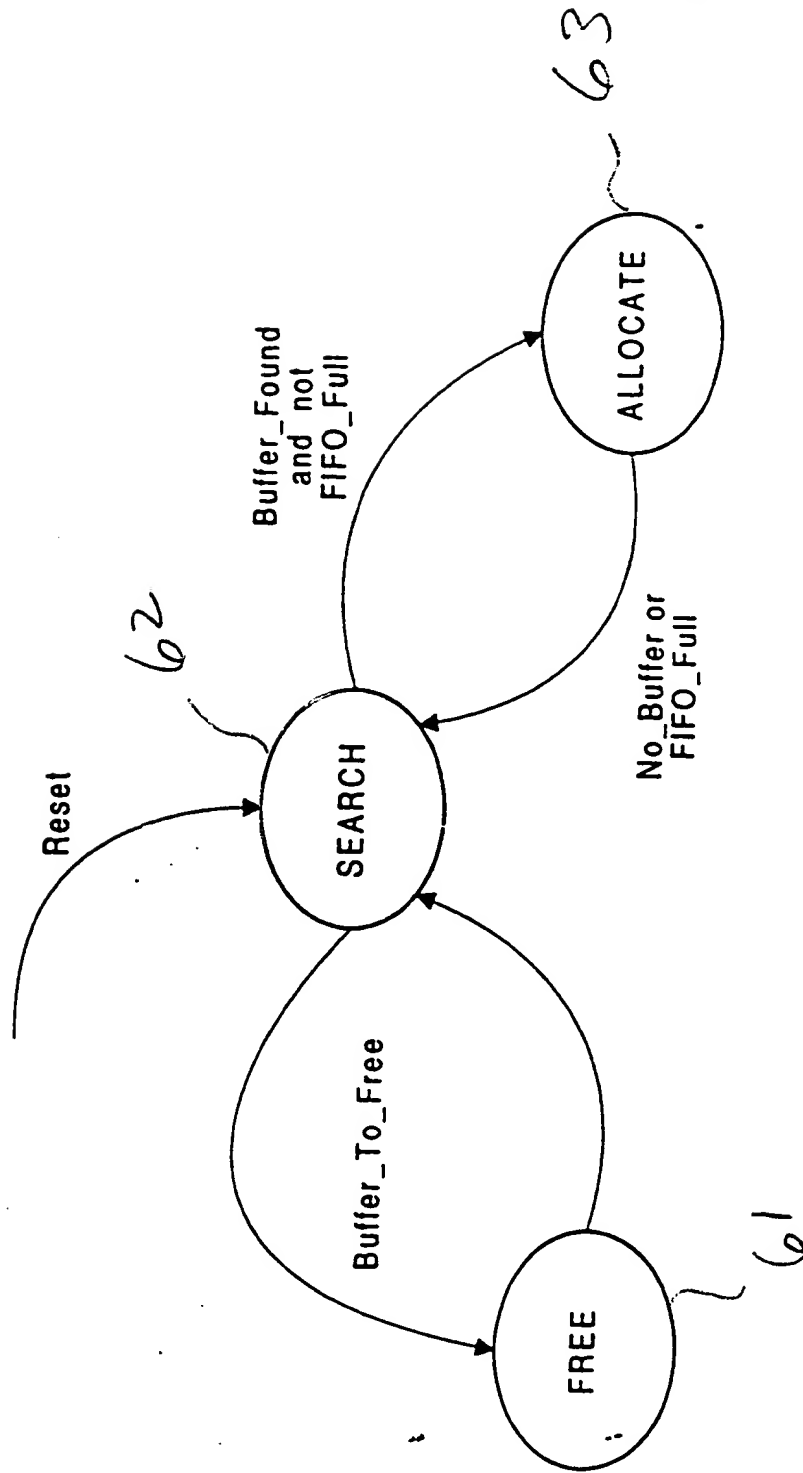


FIGURE 9